

Enrollment No:- _____

Exam Seat No:- _____

C.U.SHAH UNIVERSITY

Summer-2015

Subject Code: 4CS011CO1 Subject Name: Computer Organization & Architecture

Course Name: B.Sc. (IT)

Date: 6/5/2015

Semester: I

Marks: 70

Time: 10:30 TO 01:30

Instructions:

- 1) Attempt all Questions of both sections in same answer book/Supplementary.
 - 2) Use of Programmable calculator & any other electronic instrument prohibited.
 - 3) Instructions written on main answer book are strictly to be obeyed.
 - 4) Draw neat diagrams & figures (if necessary) at right places.
 - 5) Assume suitable & perfect data if needed.
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Q-1 Attempt the following questions. 14

1. What is Computer Organization?
2. What is Gate?
3. What is Boolean function?
4. What is minterm?
5. What is flipflop?
6. What is register?
7. Give the full form of EEPROM.

Attempt any Four Questions from (Q-2 to Q-8)

Q-2 Attempt the following questions

1. Describe Half adder with logic diagram 5
2. Explain De-Morgan's theorem 5
3. List out all the basic identities of Boolean algebra 4

Q-3 Attempt the following questions

1. Explain POS with example 5
2. Explain Don't care condition with example 5
3. Explain RAM with block diagram 4

Q-4 Attempt the following questions

1. Explain 3 to 8 line decoder in details 7
2. Explain Full Adder with example 7

Q-5 Attempt the following questions

1. Explain 4 to 1 line Multiplexer 7
2. Explain SR flipflop in details 7



- Q-6 Attempt the following questions
1. Explain DMA controller with block diagram 7
 2. Explain Register stack in details 7
- Q-7 Attempt the following questions
1. Explain Isolated I/O and Memory mapped I/O 7
 2. Explain Error detection code in details 7
- Q-8 Attempt the following questions
1. Explain General Register Organization in details 7
 2. Explain JK Flipflop and T Flipflop 7

